

The diagram illustrates a network system architecture. The upper portion provides a detailed view of a node (1), which is divided into a database section (3, 8, 13) and a processing section (2). The processing section contains a stack of modules (5, 6, 7, 9, 10, 11, 12) and a management module (MD). The lower portion shows a network topology with four source nodes (S1, S2, S3, S4) and four processor nodes (P1, P2, P3, P4) connected in a loop. A central node (R) is also shown, and the network is identified by the symbol \underline{N} .

FIG.1